

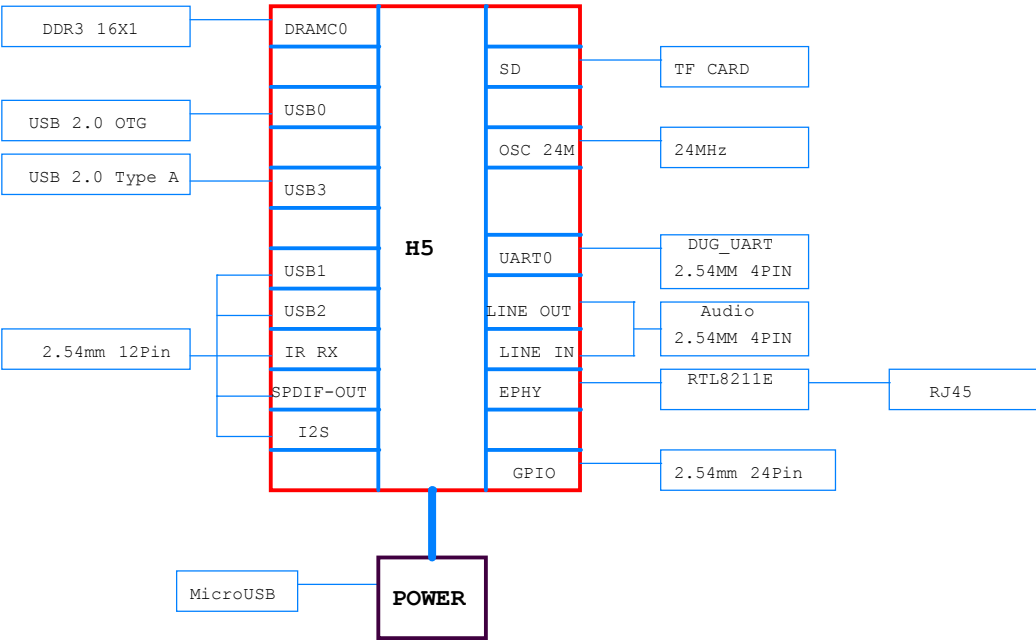
NanoPi NEO 2

Revision

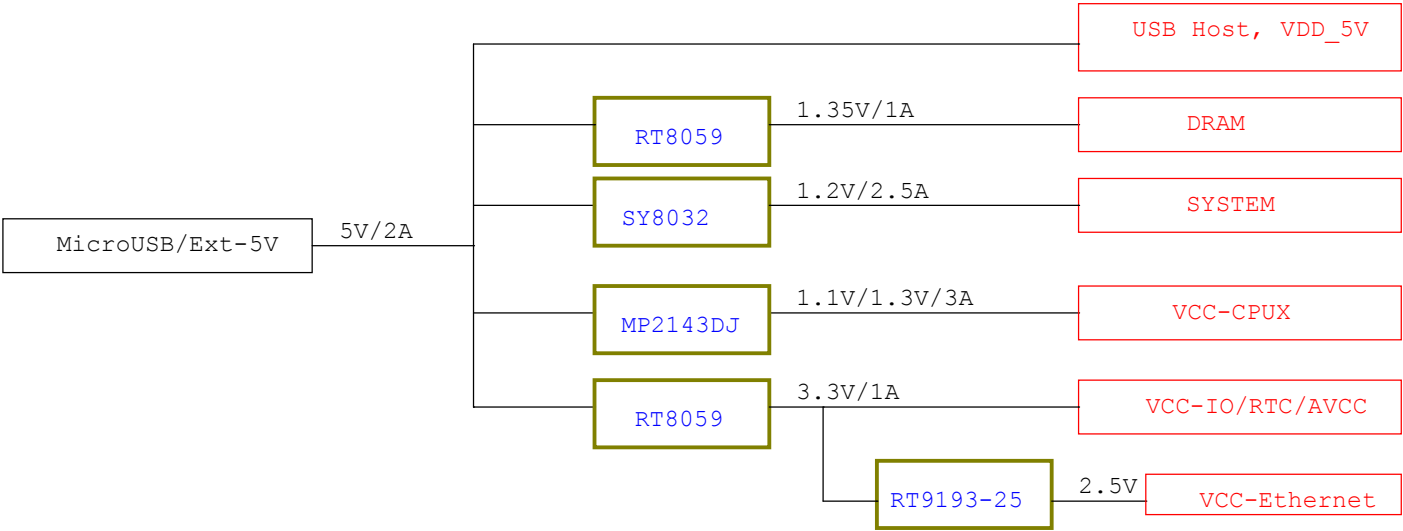
1701 , V1.0 :first release
1711 , V1.1 :Changed TF Card,
Changed Gigabit network connector,
Added GPIO Voltage regulation,
Adjusted Audio pin header,
Added CVBS



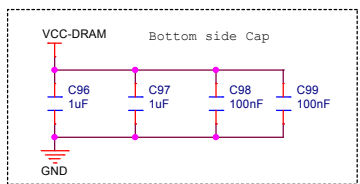
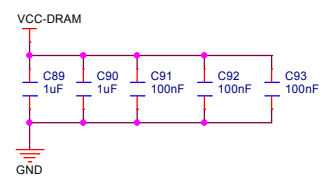
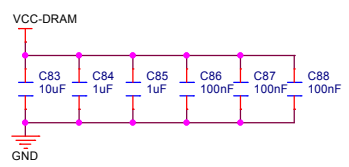
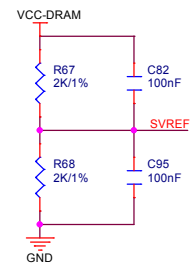
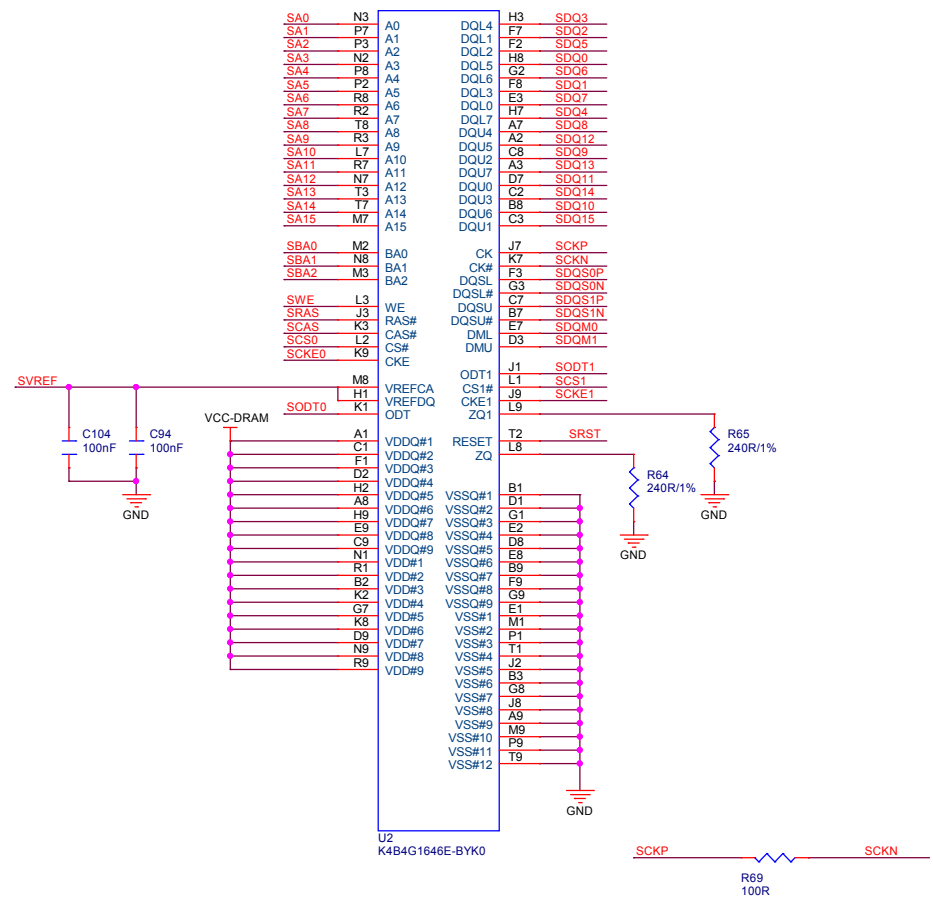
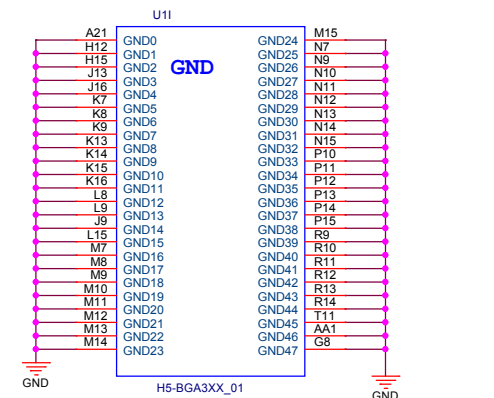
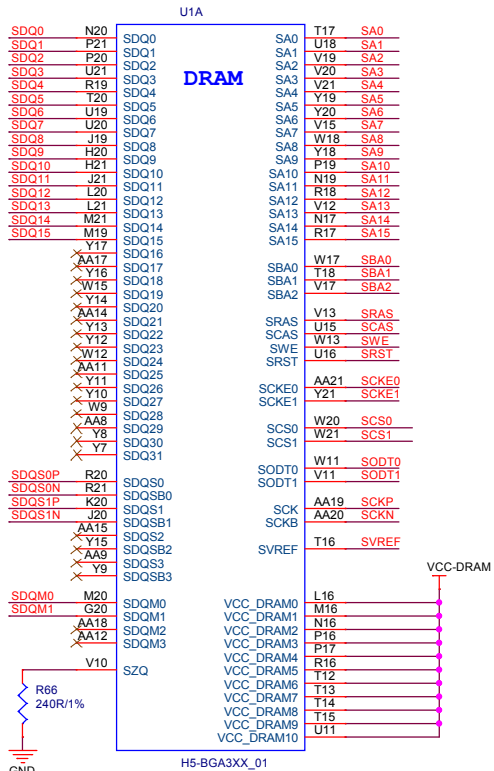
BLOCK



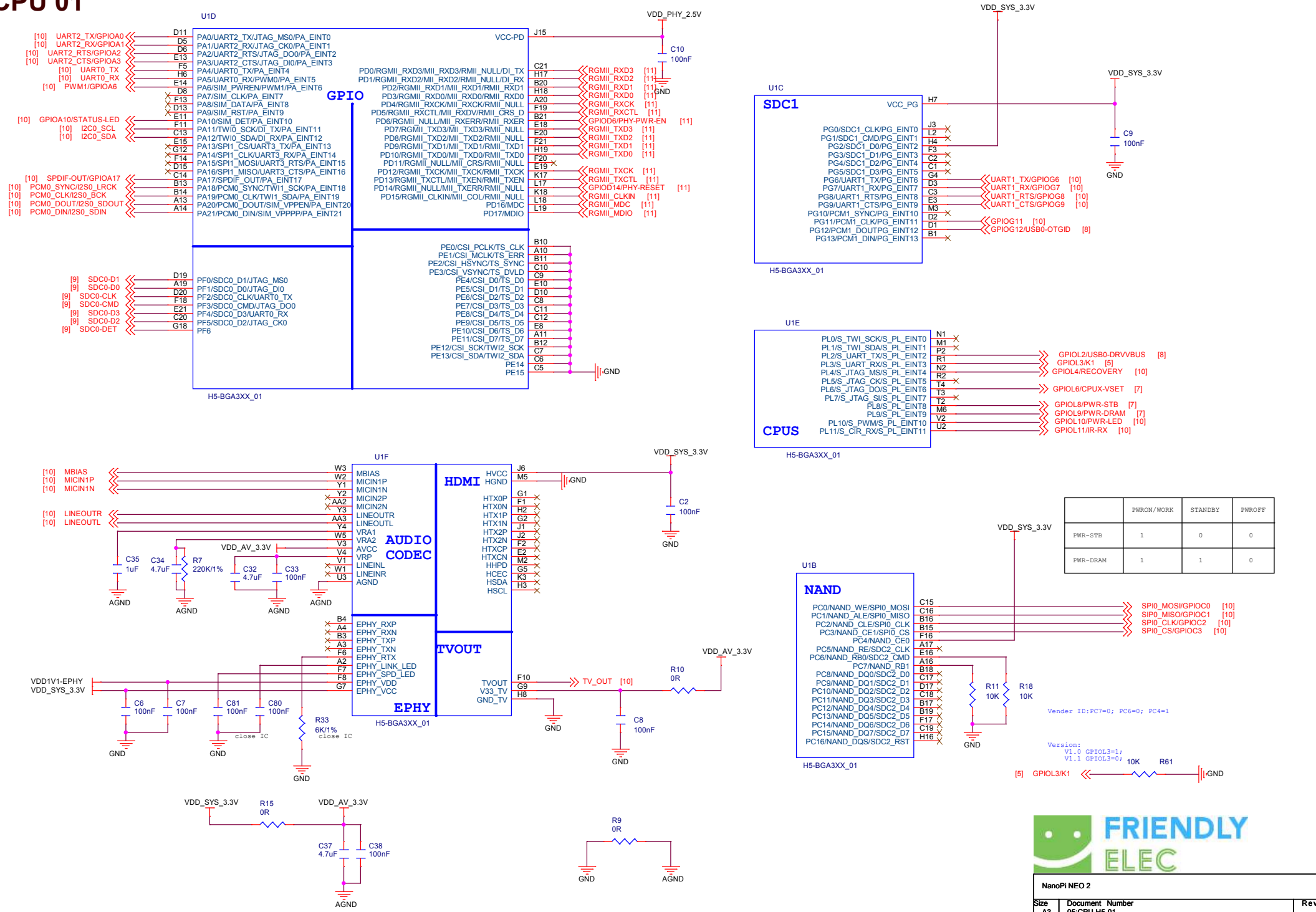
POWER TREE



DDR3 16x1

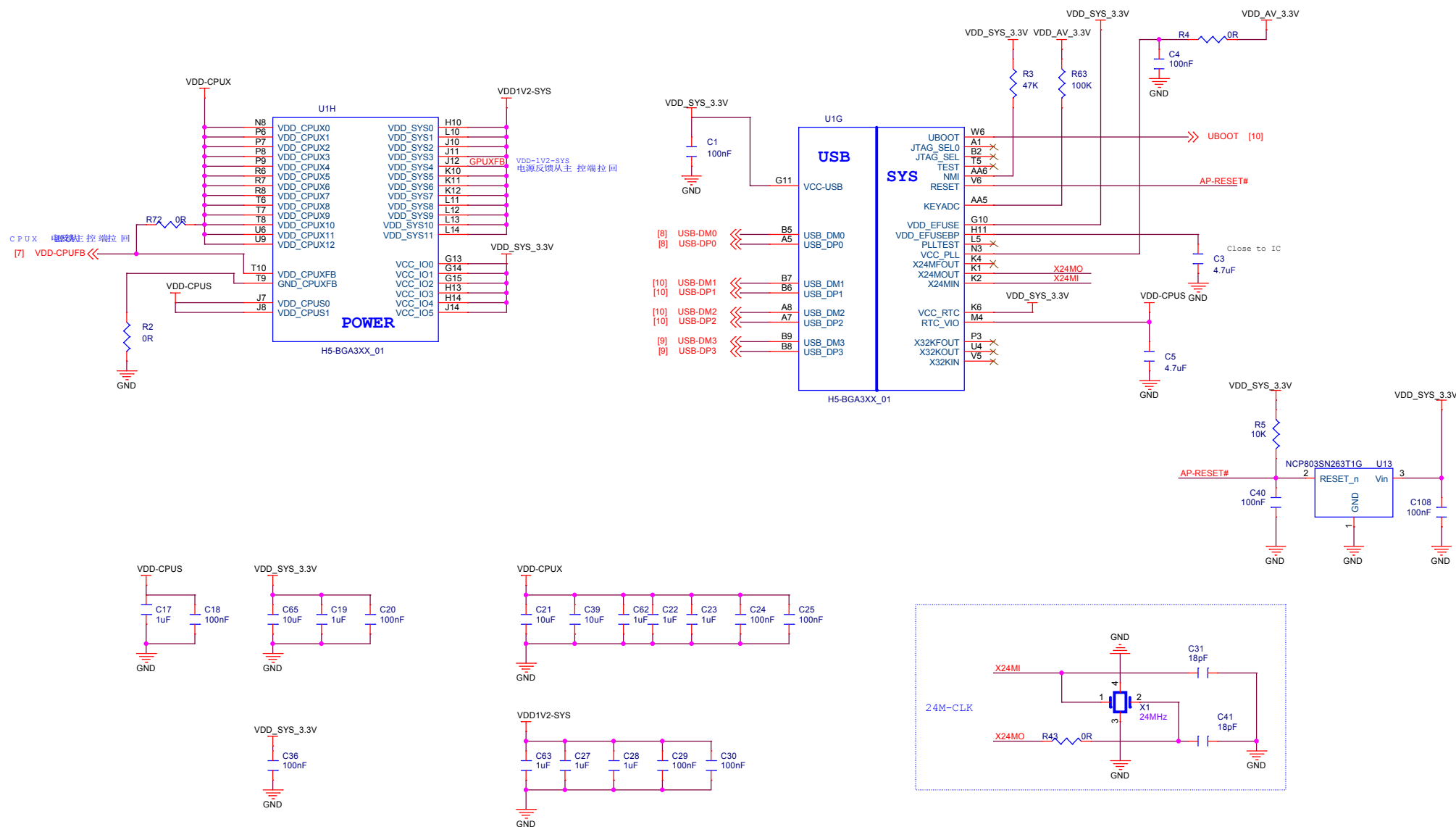


CPU 01

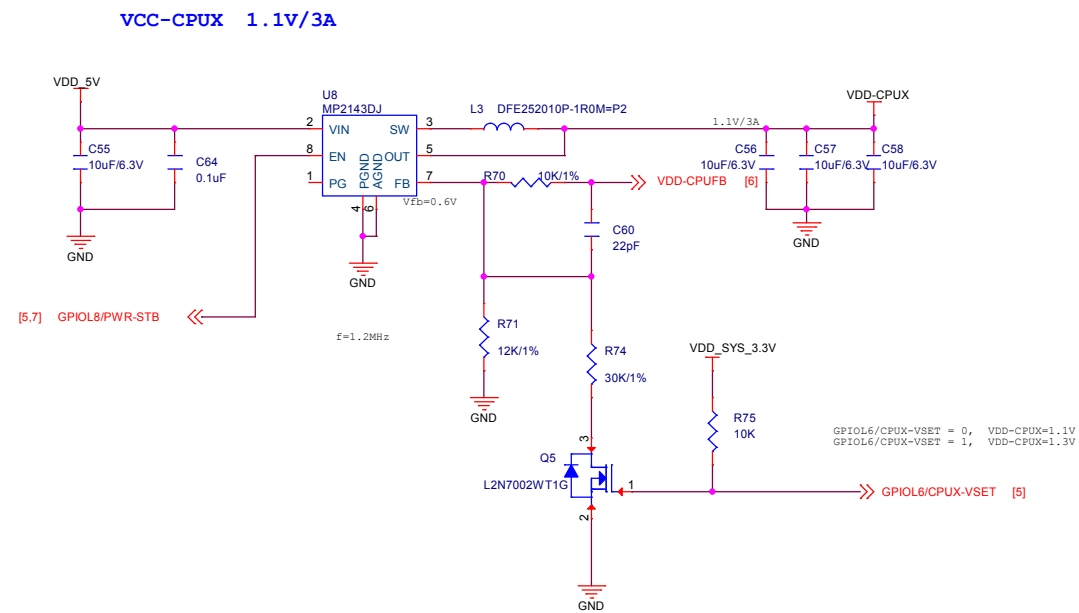
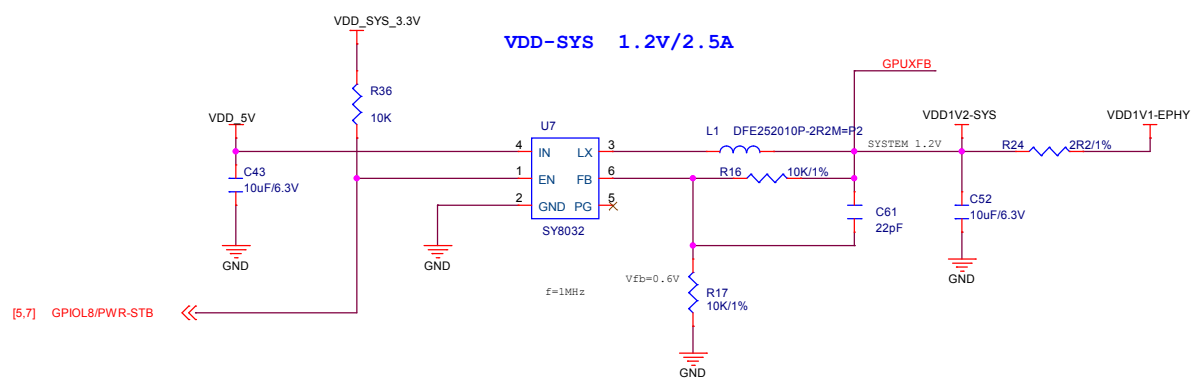
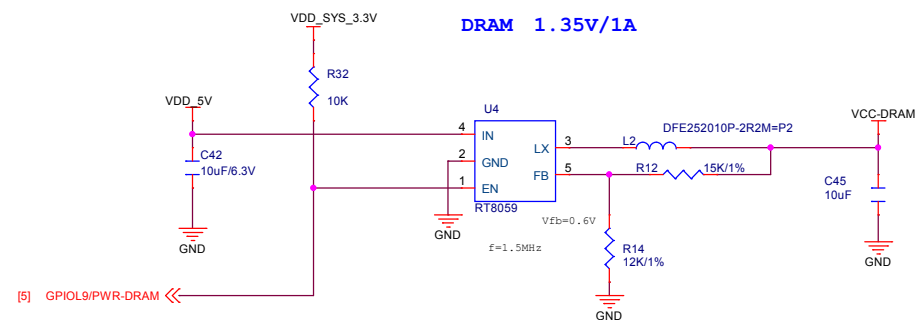


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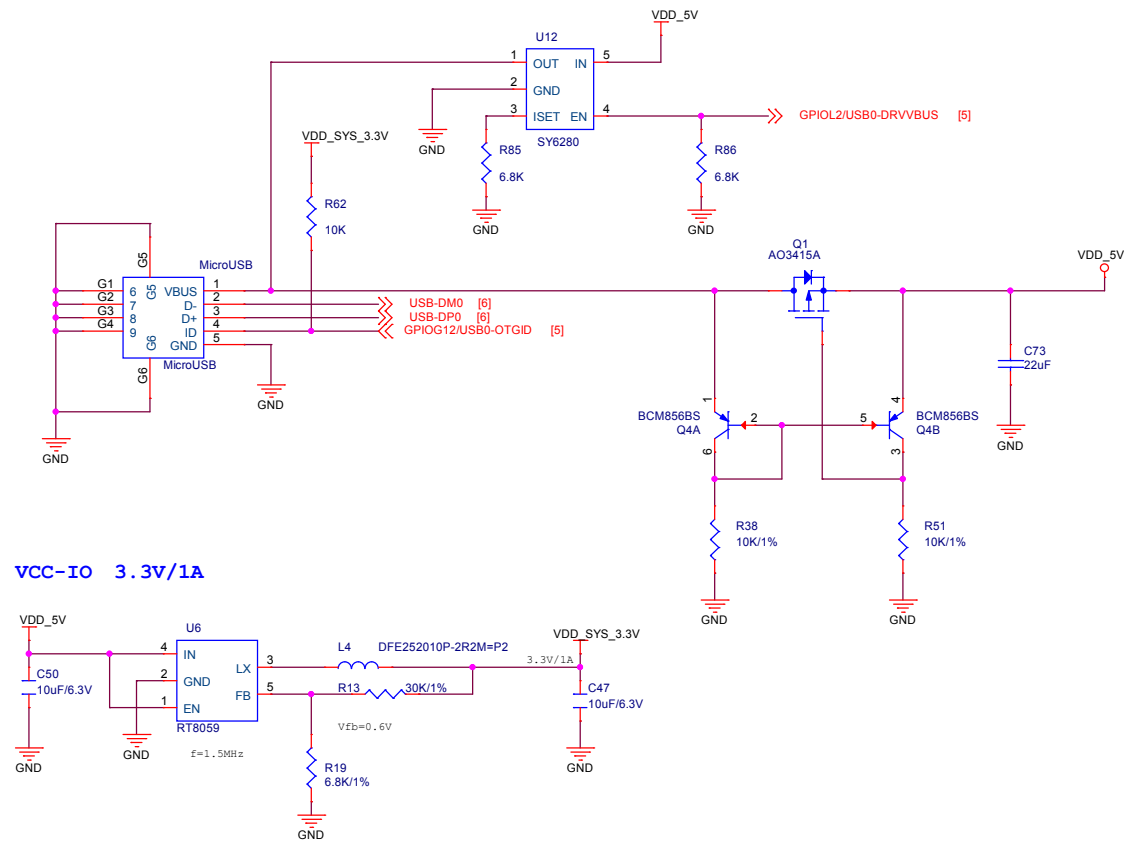
CPU 02



POWER 01



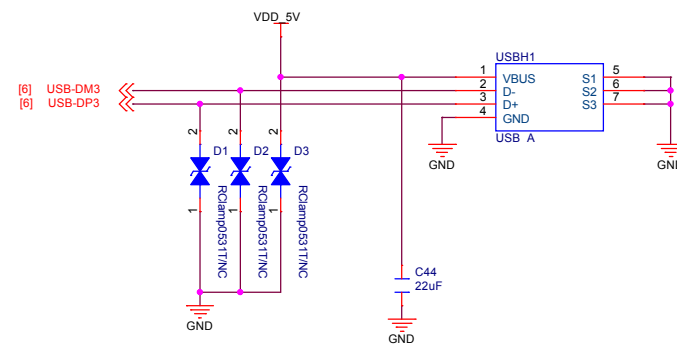
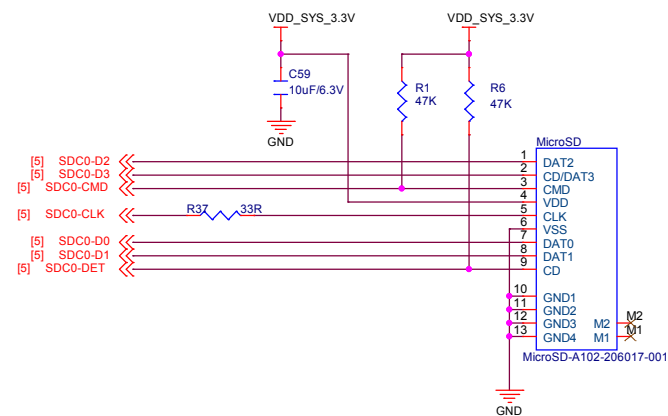
POWER 02



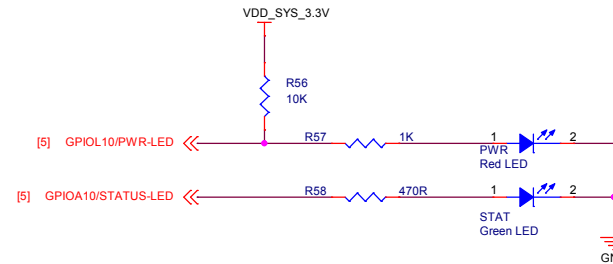
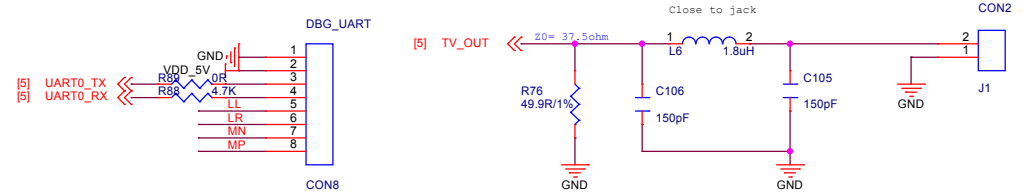
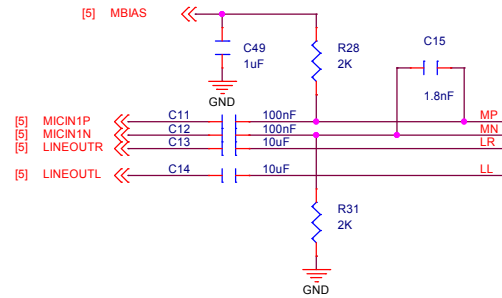
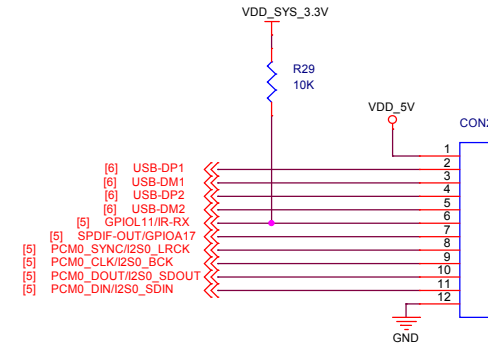
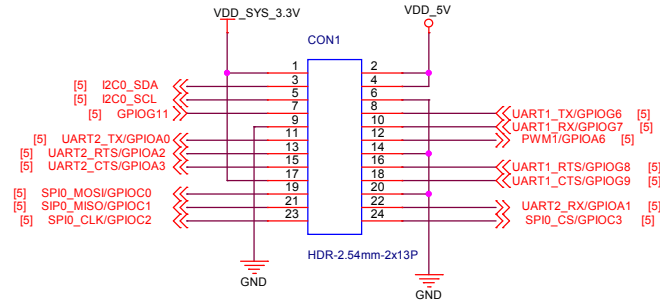
VCC-IO 3.3V/1A



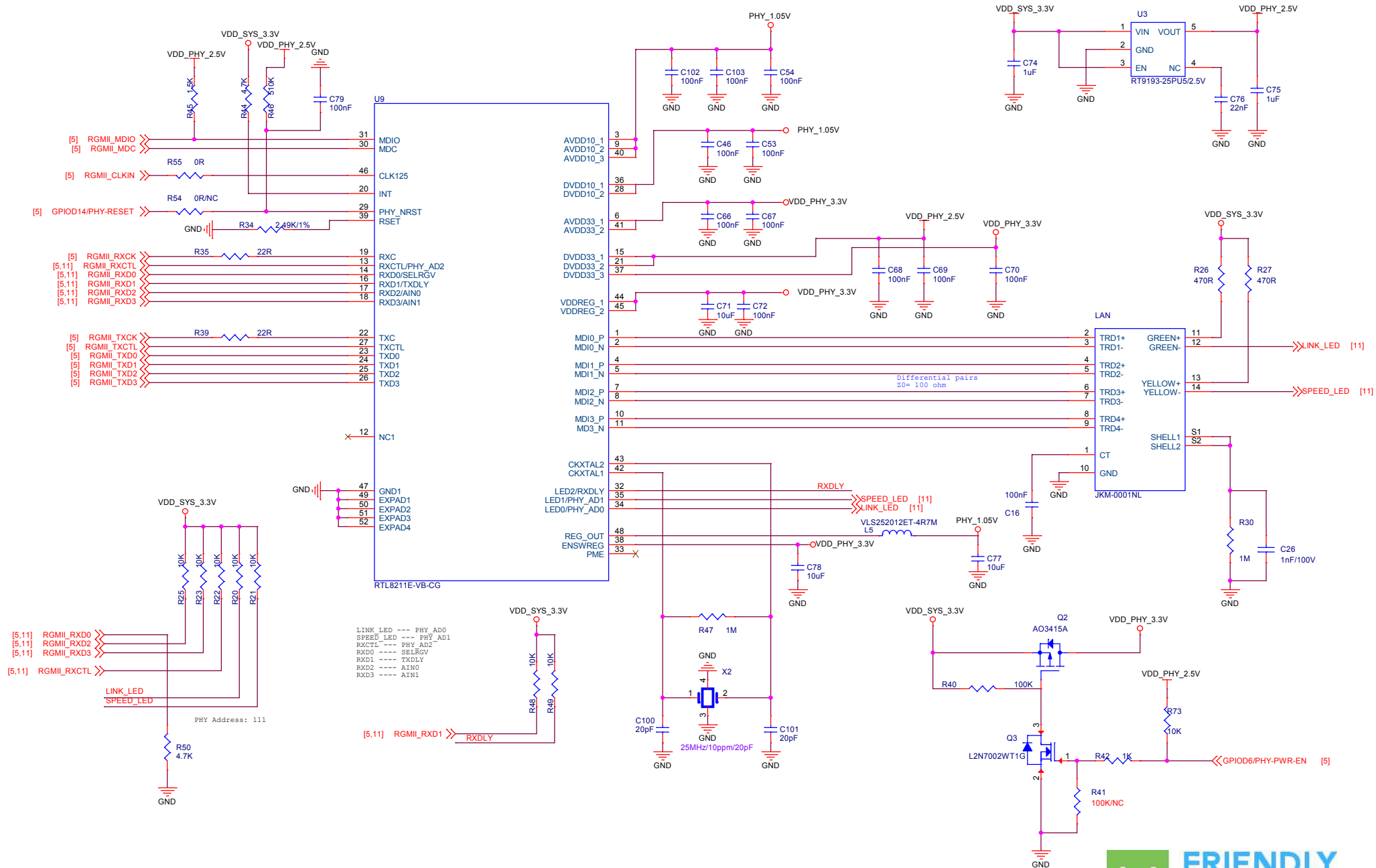
TF Card,USB Host



GPIO, DBG_UART, Audio, LEDs



10/100/1000M Ethernet



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